

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:	)	Group Art Unit: 2123
Luciano Lavagno et al.	)	Examiner: Russell L. Guill
Serial No.: 09/847,642	)	Confirmation No.: 6620
Filed: May 1, 2001	)	
For: SYSTEMS AND METHODS FOR	)	
PERFORMING SOFTWARE	)	
PERFORMANCE ESTIMATIONS	)	

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**COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE**

**Mail Stop: Issue Fee**  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Applicants wish to thank the Examiner for allowing the above-identified application.

The Examiner's Reasons for Allowance, as indicated in the December 01, 2009 Notice of Allowance, identify numerous reasons for allowance that may relate a plurality of claims. More particularly, the Reasons for Allowance purports that the currently pending claims 1-2, 4-22, 33-34, and 36-64 are allowed because the prior art references of record, Bradford (U.S. Patent No. 5,857,093), Passerone ("Fast Hardware / Software Co-Simulation for Virtual Prototyping and Trade-Off Analysis"), Hellestrand (U.S. Patent no. 6,230,114), Zivojnovic ("Compiled HW/SW Co-

Simulation”), do not teach or suggest certain elements of the currently pending claims 1, 9, 33, 41, 55, and 61-64.

For example, the Reasons for Allowance states that the prior art references, either alone or combined, do not disclose “wherein the act of annotating the software simulation model is performed during a time of the act of generating the software simulation model” for claim 1. See p. 3, Dec. 01, 2010 Notice of Allowability.

The Examiner’s statement of reasons for allowance similarly indicates similar reasons for allowance for claims 9, 33, 41, 55, and 61-64 by singling out a single claimed limitation from each of these claims. For example, the statement indicates that the prior art references either alone or in combination do not teach or suggest “where the act of associating is performed during a time of the act of parsing the assembly language software module into a data structure” of claim 55, “wherein annotating the software simulation model is invoked during a time when the means for generating the software simulation model executes” for claim 61, and “wherein the means for associating is invoked during a time when the means for parsing the assembly language software executes” of claim 63.

Nonetheless, Applicants note that the application includes independent claims, where each independent claim may recite limitations that are different from or not present in other claims. As such, Applicants respectfully submit that the independent claims and their respective dependent claims are believed to be allowable for what they respectively recite rather than whether or not the

prior art references fails to disclose any particular claimed limitations or some rephrased or paraphrased elements thereof.

If the Examiner has any questions or comments, the Examiner is respectfully requested to contact the undersigned at the number listed below.

Respectfully submitted,

Dated: March 01, 2010

By: /Erich C. Tzou/  
Erich Tzou  
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